REMARKS

Claims 1-40 remain pending in the application. The specification has been amended to provide a description of what is shown in Figure 2. No new matter has been added. Support for the amendment can be found in at least Figure 2, the text associated therewith, the original claims as filed, and the summary of the invention. Applicant respectfully requests reconsideration in view of the following remarks.

Claims 1-40 stand rejected under 35 USC 112, first paragraph, as allegedly failing to comply with the enablement requirement. Applicant respectfully traverses the rejection.

The Examiner has alleged that in claims 1, 9, 17, 25 and 33, the claimed amplifier (that) produces an intermediate signal lacks enablement, and further alleges that the specification does not explain how the amplifier circuit produces the intermediate signal and the nature of the intermediate signal. Applicant respectfully disagrees.

Applicant kindly directs the Examiner's attention to Figure 2 and the accompanying text in paragraph 34. As is shown in Figure 2, an input signal Vin 240 is applied to one input of amplifier 340 (350). The second input to amplifier 340 (350) is a feedback signal that is produced from the output of the emitter of transistor 365 (375) of a first stage 310 of the variable gain amplifier. Similarly, outputs from other corresponding emitters in the other stages can as well be coupled to the second input of amplifier 340 (350) as is shown in the inset detail associated with the respective stages shown in Figure 2.

As can be seen in Figure 2, the output of amplifier 340 produces a signal, the claimed intermediate signal, that is provided as an input to the base of transistor 365 (375) (and similarly to the bases of transistors in other stages as shown in the inset). The gain stage 310 has a variable offset, as described in paragraph 34 of Applicant's specification, whose effect is decreased by the use of the amplifier 340 (350). Accordingly, the gain stage 310 as configured can be referred to as a variable offset circuit. The gain stage 310 includes transistors 340 (350) that receive the signal produced by the amplifier 340 (350) (the intermediate signal) and produce a feedback signal that is provided as an input to the amplifier 340 (350) as is set forth in Applicant's claim 1.

Support for claim 1 and the claimed intermediate signal also can be found in the summary of the invention at least at paragraph 14 and in the original claims themselves. Applicant respectfully asserts that the figures and associated text are sufficiently clear to teach one of ordinary skill in the art applicant's claimed structure. In order to ensure the clarity of the disclosure for the purposes of examination, Applicant has amended the specification to include a specific recitation of the structures shown in originally presented Figure 2. No new matter has been presented. Applicant respectfully asserts that claim 1 as presented is sufficiently enabled by the original specification including attending figures. Accordingly, Applicant respectfully requests withdrawal of the 35 USC 112 rejection.

Claims 2-8 depend from claim 1 and are allowable for at least the same reasons discussed above with respect to claim 1.

Claim 9 is directed to a circuit that includes amplifying means for producing an intermediate signal. Applicant respectfully asserts that claim 9 is also sufficiently enabled by the original specification and figures as discussed above with regard to claim 1.

Claims 10-16 depend from claim 9 and are allowable for at least the same reasons discussed above with respect to claim 9.

Claim 17 is directed to a wireless transceiver that includes an amplifier operable to produce an intermediate signal. Applicant respectfully asserts that claim 17 is also sufficiently enabled by the original specification and figures as discussed above with regard to claim 1.

Claims 18-24 depend from claim 17 and are allowable for at least the same reasons discussed above with respect to claim 17.

Claim 25 is directed to a wireless transceiver that includes amplifying means for producing an intermediate signal. Applicant respectfully asserts that claim 25 is also sufficiently enabled by the original specification and figures as discussed above with regard to claim 1.

Claims 26-32 depend from claim 25 and are allowable for at least the same reasons discussed above with respect to claim 25.

Claim 33 is directed to a method that includes amplifying an input signal to produce an intermediate signal. Applicant respectfully asserts that claim 33 is also sufficiently enabled by the original specification and figures as discussed above with regard to claim 1.

Claims 34-40 depend from claim 33 and are allowable for at least the same reasons discussed above with respect to claim 33.

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Respectfully submitted,

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